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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
AS RECEIVING OFFICE FOR THE PATENT COOPERATION TREATY**

In re Application of )

WANDEL & GOLTERMANN et al. )

Serial No.: PCT/GB99/01339 )

Filed: 29 April 1999 )

For: JITTER MEASUREMENT )

File No.: 20251-000100US )

**PRELIMINARY AMENDMENT**

Commissioner of Patents  
and Trademarks  
Box PCT  
Washington, DC 20231

Dear Sir:

Prior to examination, please amend the claims as follows:

**IN THE CLAIMS:**

(All claims are set forth for convenient reference.)

1. (Amended) A method of measuring jitter in a digital signal comprising:  
forming an offset reference clock signal [(101, 201, 103),] being offset by a  
predetermined frequency amount from said digital signal;

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